LARC CHARACTER CODES

CONSOLE DECIMAL DISPLAY	NUMERIC ONE DIGIT	ALPHA TWO DIGIT	CONSOLE KEYBO ARD
0	11100	15	1
θ	00100	16	^
_	00010	17	_
0	10000	20	. 0
i	00001	21	1
2	10011	22	2
3	00111	23	3
4	10110	24	4
5	01000	25	5
6	11001	26	6
7	01011	27	7
8	11111	28	8
9	01110	29	9
8	11030	37	
+	10101	80	+
	01101	Illegal	

INSTRUCTION WORD FORMAT

TIIAABBMMMM

T: 1-9 = TRACING MODE

= NO TRACING MODE

= INDIRECT ADDRESSING MODE

I: INSTRUCTION DIGITS

A: ARITHMETIC REGISTER ADDRESS

B: INDEX REGISTER ADDRESS

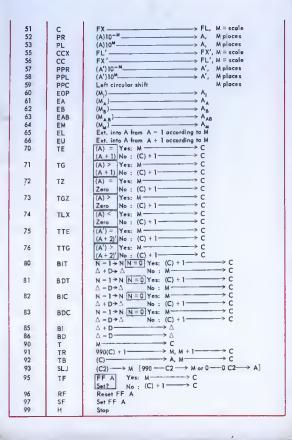
M: STORAGE ADDRESS

 $\stackrel{Univac^*}{ ext{LARC}}$

INSTRUCTION CARDS

COMPUTING UNIT

00	SKP	Skip	
01	AX	(M) + (A)	—> A
02	A	(M) (A)	> A
03	AM	I (M) I ⊕ (A)—	
04	AU	(M) (+) (A)	- A + 1
05	AAX	1411 + 1411	- A'
06	AA	(M') ① (A')	A'
09	FV	(M) (M) (A)	→ A, Reset Lock & Connect
09	FV		A, Reset Lock & Connect
	N/M	Set? No : M-	> C
11	NX	-(M) + (A)	—→ A
12	N	-(M) ⊕ (A)	→ A
14	NU	-(M) (+) (A)	—→ A + 1
15	NNX	-(M') + (A')	—→ A'
16	NN	-(M') ⊕ (A')	—→ A'
19	FVK	Lock Yes: (12DD)	
		Set? No : M	—→ C
20	MXR	(M) × (A)Rdd	→ A
21	MXE	(M) × (A)	> A'
22	MR	(M) ⊗ (A)Rdd —	A
23	M	(M) (A)	A
24	MU	(M) ⊗ (A)—	A 4 1
25	ME	(M) & (A)	- A'
26	MMX	(M') × (A')	-
27	***************************************	(M') & (A')	
	MM	Lock Yes: M	→ A
29	SV	Lock Yes: M	
		Set? No : (A)————————————————————————————————————	→ 2DD
30	DX	(A) ÷ (M)—————	—→ A
31	DXE	(A) ÷ (M) —	A, A + 1 [Remoinder]
32	DR	(A) (A) (M)Rdd	—→ A
34	DUR	(A)	> A + 1
35	DDX	(A') ÷ (M')————————————————————————————————————	—→ A'
36	DD	(A') ⊕ (M')————	> A'
37	DSE	(A') (M)	
39	SVK	Lock Yes: M	—→ C
		Set? No : (A)	—→ 12DD
40	S	(A) -	— → M
41	SN	-(A)	—→ M
42	SM	1(A)1	M
43	F	(M)	A
45	SS	(A')	—→ M'
46	SSN	-(A')	M'
47	SSM	I(A')I	M'
48	FF	(14)	- A'
50	CX	(M') ————————————————————————————————————	EV M-asala
20	CX		→ r A, M - scale



FLIP-FLOPS

NUMBER	CONSOLE OESIGNATION	NAME
00-09	0-9	Sense
10	018	Oisclosure
11	10P	Processor Intervention †
15	II	Manual and IOP Intervention Inhibit
20	TM	Enter Tracing Mode ‡
21-29	1-9	Tracing Mode
30-34	0-4	Computing Unit Manual Intervention †
38	TAPE	Improper Paper Tape‡
39	A00	Improper Operand†
40	ZERO	Zero floating point adder result t
41	017	Non-normalized divisor t
42	EX 🕈	Exponent overflow †
43	EX ♦	Exponent underflow†
44	0F	Fixed decimal overflow †
45	SGN	Sign anomaly†
46	STALL	Stall ‡
47	MISC	Control Error‡
48	RES	A-register control error on result time ‡
49	OEC	Oecoding error on tracing digit‡
50	CALL	B-adder error to memory address decoder \$
51	10 E	Instruction error ‡ ·
52	00E	Operand error ‡
53	В	A-register error on B-modification ‡
54	M	A-register error on M-slot ‡ *
55	W	A-register error on result time ‡
56	C1	B-adder error to C1, HSB, or AU‡
57	1R2	B-adder error to A-storage, A-selector, or M of IR2‡
58	C2	B-adder error to C2
59	AB	AB-adder error‡

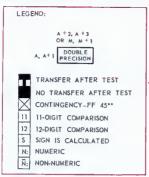
60	AS-	AS-register error ‡
60 61	COMP	Comparator error ‡
62	QIE	Multiplier, quotient, or extraction
62	QIE.	error ‡
63	SFC	Shift controls error ‡
64	0F	Overflow error ‡
65	PC	Program counter or decoding error \$
66	EP	Ending pulse error ‡
57	AH	AH-register error ‡
68	AD	AO-register error ‡ .
69	SGN	Sign position error ‡
70	A	A-register error on A-slot‡
71	1	Oigit #1 error
72	2	Oigit #2 error
73	3	Oigit #3 error
74	4	Oigit #4 error
75	5	Oigit #5 error
76		Digit #6 error
77	6 7	Oigit #7 error
78	8	Oigit #8 error
79	9	Oigit #9 error
80	10	Oigit #10 error
81	11	Oigit #11 error
82	12	Oigit #12 error
84	CY	Cycling unit error
90	R (on Flex)	Start paper tape
98	Master Check	Master error‡
99	Master Contingency	Master contingency †

NOTE: † Contingency Flip Flop

‡ Error Flip Flop

RULES FOR SIGN POSITION

1. STORE INSTRUCTIONS: SM, SN, SSN



0
0
0
0
0

	SN,	SSN*
	0	_
	-	0
Α		
	N	\times
	Ñ	> <

^{**}WHEN SIGN CONTINGENCY OCCURS A ZERO → S
*SIGN OF EACH HALF IS HANOLEO SEPARATELY.

II. EQUALITY & MAGNITUDE TESTS: TE, TTE, TZ, TGZ, TLZ, TG, TTG







٠	TG, TTG*									
		0	-		N	Ñ				
	0	11	Т	11		X				
	~		11			X				
^	٠	11	П	11		X				
	N	П			12	X				
	Ñ	X	\times	X	\times	X				





III. ARITHMETIC INSTRUCTIONS: FIXED AND FLOATING POINT

1. AOO ANO SUBTRACT: AX, AM, NX, A, AU, AAX, AA, N, NU, NNX, NA

	AX M							
		0	-	•	N	Ñ		
	0	0	S.	0	N	X		
A	_	\$			N	X		
		0	_		N	X		
	N	N	N	Ν	X	X		
	Ñ	X	\times	X	X	X		



NX M									
	0	-		N	Ñ				
0	5	0	0	И					
_	_	s	٥	N					
	_	0	•	N.	/				
N	И	Ν	N						
Ñ									
	0 - 2 12	_	0 - 0 \$ 0 \$	0	0 - · N 0 S O O N S - N				

	A, AU, AAX, AA							
		0	-	·	N	N		
	0	0	S	0	X	X		
A	-	5	-		X	X		
		0	-	•	X	X		
	N	X	X	X	X	X		
	Ñ	X	X	X	X	X		
		_				_		

	N	, NI	U, 1	INX,	, NI	N
		0	÷	٠	N	N
	0	s	0	0	X	X
Α	-		5	-	X	\boxtimes
^		_	0		X	X
	N	X	\times	Х	X	\times
	Ñ	X	\times	X	X	\times

2. MULTIPLY AND DIVIDE:

MXR, MXE, MR, M, MU, ME, MMX, MM, OX, OXE, OR, OUR, OOX, OO, OSE

	м					
		0	_		Ν	Ñ
A	0	0	-	•	X	X
	-	-	0	·	\times	X
	•	•	•	•	X	X
	N	\boxtimes	X	X	X	X
	Ñ	X	X	X	X	X